

```
Memory;
Partno TMD8001;
Revision 01;
Date 8/20/2006;
Designer Len Bayles;
Company TMD Innovations;
Location None;
Assembly None;
Device g16v8a;
```

```
*****/
/*
/* ROM/RAM addressing.
/*
/*
*****/
```

```
/*
* Inputs:
*/
```

```
Pin 1 = SW1;
Pin 2 = SW2;
Pin 3 = A11;
Pin 4 = A12;
Pin 5 = A13;
Pin 6 = PINT;
```

```
/*
* Outputs:
*
*/
```

```
Pin 12 = ROM;
Pin 13 = RAM;
```

```
/*
* Logic:
*/
```

```
ROM = PINT # !((SW1 & !SW2 & !A11 & !A12 & !A13) # (!SW1 & SW2 & !A12 & !A13) # (SW1 & SW2 & !A13));
RAM = PINT # ((SW1 & !SW2 & !A11 & !A12 & !A13) # (!SW1 & SW2 & !A12 & !A13) # (SW1 & SW2 & !A13));
```