

```
name IO;
Partno TMD8002;
Revision 01;
Date 8/20/2006;
Designer Len Bayles;
Company TMD Innovations;
Location None;
Assembly None;
Device gl6v8a;
```

```
/*
 * I/O Control
 */
*****
```

```
/*
 * Inputs:
 */
```

```
Pin 1 = IOR;
Pin 2 = IOW;
Pin 3 = A9;
Pin 4 = A10;
Pin 5 = A11;
Pin 6 = A12;
Pin 7 = A13;
```

```
/*
 * Outputs:
 */
```

```
Pin 12 = OUTP0;
Pin 13 = INP0;
Pin 14 = OUTP1;
Pin 15 = INP1;
Pin 16 = OUTP2;
Pin 17 = INP2;
Pin 18 = OUTP3;
Pin 19 = INP3;
```

```
/*
 * Internal Logic:
 */
```

```
in = !A12 & !A13;
out0 = A12 & !A13;
out1 = !A12 & A13;
out2 = A12 & A13;
```

```
zero = !A9 & !A10 & !A11;
one = A9 & !A10 & !A11;
two = !A9 & A10 & !A11;
three = A9 & A10 & !A11;
four = !A9 & !A10 & A11;
five = A9 & !A10 & A11;
six = !A9 & A10 & A11;
seven = A9 & A10 & A11;
```

```
/*
 * Logic:
 */
```

```
OUTP0 = out0 & zero & IOW;
INP0 = !(in & zero & IOR);
OUTP1 = out0 & one & IOW;
INP1 = !(in & one & IOR);
OUTP2 = out0 & two & IOW;
INP2 = !(in & two & IOR);
OUTP3 = out0 & three & IOW;
INP3 = !(in & three & IOR);
```